

(Use several sheets if necessary)

ND-34

09/551,400

Wataru DOMON

April 17, 2000

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[illegible][illegible]

N7	Japanese Office Action dated February 5, 2002, with partial English translation.
U	Takashi Sato, "Solution for Cycle Master Location Limitation", pp. 1-14, January 26, 1998.

DATE CONSIDERED

SHEET 1 OF 2

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ND-34

09/551,400

Applicant(s)

Wataru DOMON

Filing Date

April 17, 2000

Group Art Unit

2739

[illegible][illegible]

17		Takashi Sato, "Synchronizing Cycle Master to External Timing Information via Cycle Slave", pp. 1-12, March 19, 1998.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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ND-342US

-09/551,400

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[illegible]

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ND-342U

09/551,400

Applicant(s)

Wataru DOMON

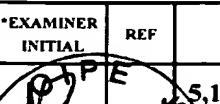
Filing Date

April 17, 2000

Group Art Unit

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		5,128,971	07/07/1992	Johnson			

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FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

M	European Search Report dated April 16, 2003.
N	Masatoshi Ueno, et al. "Cycle Reconfiguration for a Bridge". FIREWIRE/IEEE-1394, 'Online!', January 26, 1998, pp. 2-4.

EXAMINER

R. Pizano

DATE CONSIDERED

7/8/03

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INFORMATION DISCLOSURE CITATION

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Docket Number (Optional)

ND-342US

Application Number

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Applicant(s)

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Group Art Unit

2739-2661

*EXAMINER

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Takashi Sato, "Synchronizing Cycle Master to External Timing Information Via Cycle Slave", Synchronizing Cycle Master to External Timing Information Via Cycle Slave, March 19, 1998, pp. 1-12.

Masa Akahane, et al., "Study on Net Manager Selection Algorithm", FIREWIRE/IEEE-1394, 'Online!', June 9, 1998, pp. 3-22.

Institute of Electrical and Electronics Engineers, "IEEE Standard for a High Performance Serial Bus", IEEE Standard for a High Performance Serial Bus, IEEE STD 1394-1995, pp. i, 48, 88, 234-241.

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R. Pizzommi

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